



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **AKAMATSU, et al.**

Group Art Unit: **2827**

Serial No.: **09/392,722**

Examiner: **D. E. GRAYBILL**

Filed: **September 9, 1999**

Confirmation No.: **1679**

**For: INTEGRATED ELECTRONIC DEVICE HAVING FLIP-CHIP CONNECTION
WITH CIRCUIT BOARD AND FABRICATION METHOD THEREOF**

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: December 15, 2003

Sir:

In response to the Office Action dated **September 17, 2003**, please amend the above-identified application as follows: